

REMARKS

In the final Office Action, the Examiner rejected claims 14, 16, 17, 22, 23, and 27 under 35 U.S.C. § 102(b) as anticipated by Boesch et al. (U.S. Patent No. 6,188,877, hereinafter "BOESCH") and rejected claims 14-29 under 35 U.S.C. § 102(b) as anticipated by Adar (U.S. Patent No. 5,774,017 hereinafter "ADAR").

By way of this Amendment, Applicants propose amending claims 14, 16, 17, 19-23, 27, and 29 to improve form. Applicants traverse the Examiner's rejections under 35 U.S.C. § 102 in light of the amendments provided herein. Claims 14, 16, 17, and 19-29 remain pending.

REJECTION UNDER 35 U.S.C. § 102 BASED ON BOESCH

In paragraph 5 of the Office Action, the Examiner rejected claims 14, 16, 17, 22, 23, and 27 under 35 U.S.C. § 102(b) as allegedly anticipated by BOESCH. The rejection is respectfully traversed.

A proper rejection under 35 U.S.C. § 102 requires that a single reference teach every aspect of the claimed invention. Any feature not directly taught must be inherently present. See M.P.E.P. § 706.02. BOESCH does not disclose the combination of features recited in amended claims 14, 16, 17, 22, 23 and 27.

For example, proposed amended independent claim 14 is directed to a method, of reducing a negative influence on signals transmitted in one of at least two frequency bands provided by a same cellular network, that comprises providing a first connection between a signal generating chip and a signal processing chip; providing a second connection between the signal generating chip and the signal processing chip;

transmitting or receiving, by the signal generating chip, signals in a first frequency band, of the at least two frequency bands provided by the same cellular network, on the first connection; transmitting or receiving, by the signal generating chip, signals in a second frequency band, of the at least two frequency bands provided by the same cellular network, on the second connection; when transmitting or receiving the signals in the first frequency band on the first connection, breaking the second connection between the signal generating chip and the signal processing chip; and when transmitting or receiving the signals in the second frequency band on the second connection, breaking the first connection between the signal generating chip and the signal processing chip.

BOESCH does not disclose the combination of features recited in proposed amended claim 14. For example, BOESCH does not disclose breaking a second connection between a signal generating chip and a signal processing chip when transmitting or receiving signals in a first frequency band on a first connection; and breaking the first connection between the signal generating chip and the signal processing chip when transmitting or receiving signals in a second frequency band on the second connection, as recited in claim 14.

BOESCH illustrates several embodiments of an amplifier circuit in the figures. BOESCH does not specifically disclose that the amplifier circuit is implemented in a chip. Nevertheless, even assuming that it is reasonable to interpret the disclosure of BOESCH as disclosing that the amplifier circuit is implemented in a chip (a point that Applicants do not concede), BOESCH does not specifically disclose that the amplifier circuit (chip) is connected to a signal generating chip via first and second connections.

In the embodiment of Figure 7, for example, BOESCH discloses a 1900 MHz RF input 405 and a 800 MHz RF input 415. Even assuming, for the sake of argument, that it is reasonable to interpret the disclosure of BOESCH as disclosing a signal generating chip as the component generating the 1900 MHz input 405 and the 800 MHz input 415, that the 1900 MHz input 405 is provided via a first connection, and that the 800 MHz input 415 is provided via a second connection (points that Applicants do not concede), BOESCH does not disclose breaking the second connection between the signal generating chip and the signal processing chip when transmitting or receiving the signals in the first frequency band on the first connection, and breaking the first connection between the signal generating chip and the signal processing chip when transmitting or receiving the signals in the second frequency band on the second connection, as recited in claim 14.

Rather, BOESCH discloses a switching network 418 that includes switches 422, 424, and 426 (Figure 4; col. 7, lines 15-20). BOESCH discloses that switching network 418 is part of the amplifier circuit (Figure 4). Thus, the switching network 418, of BOESCH, cannot break a first connection provided between a signal generating chip and a signal processing chip, or a second connection provided between the signal generating chip and the signal processing chip, as recited in claim 14. Therefore, BOESCH does not disclose breaking the second connection between the signal generating chip and the signal processing chip when transmitting or receiving the signals in the first frequency band on the first connection; and breaking the first connection between the signal

generating chip and the signal processing chip when transmitting or receiving the signals in the second frequency band on the second connection, as recited in claim 14.

Similarly, with regard to Figure 7, BOESCH discloses a switching circuit 725 that is provided as part of the amplifier circuit (col. 10, lines 14-29). Thus, the switching circuit 725, of BOESCH, cannot break a first connection provided between a signal generating chip and a signal processing chip, or a second connection provided between the signal generating chip and the signal processing chip, as recited in claim 14.

Therefore, BOESCH does not disclose breaking the second connection between the signal generating chip and the signal processing chip when transmitting or receiving the signals in the first frequency band on the first connection; and breaking the first connection between the signal generating chip and the signal processing chip when transmitting or receiving the signals in the second frequency band on the second connection, as recited in claim 14.

For at least the foregoing reasons, Applicants respectfully submit that claim 14 is not anticipated by BOESCH.

Amended independent claims 16, 17, 22, 23, and 27 recite features similar to, yet possibly different in scope than, the features described above with regard to claim 14. Accordingly, claims 16, 17, 22, 23, and 27 are not anticipated by BOESCH for at least reasons similar to the reasons given with regard to claim 14.

Accordingly, Applicants respectfully request the Examiner's reconsideration and withdrawal of the rejection of claims 14, 16, 17, 22, 23, and 27 under 35 U.S.C. § 102 based on BOESCH.

REJECTION UNDER 35 U.S.C. § 102 BASED ON ADAR

In paragraph 6 of the final Office Action, the Examiner rejected pending claims 14, 16, 17, and 19-29 under 35 U.S.C. § 102(b) as allegedly anticipated by ADAR. The rejection is respectfully traversed.

As explained above, a proper rejection under 35 U.S.C. § 102 requires that a single reference teach every aspect of the claimed invention. Any feature not directly taught must be inherently present. See M.P.E.P. § 706.02. ADAR does not disclose or suggest the combination of features recited in pending claims 14, 16, 17, and 19-29.

For example, proposed amended independent claim 14 is directed to a method, of reducing a negative influence on signals transmitted in one of at least two frequency bands provided by a same cellular network, that comprises providing a first connection between a signal generating chip and a signal processing chip; providing a second connection between the signal generating chip and the signal processing chip; transmitting or receiving, by the signal generating chip, signals in a first frequency band, of the at least two frequency bands provided by the same cellular network, on the first connection; transmitting or receiving, by the signal generating chip, signals in a second frequency band, of the at least two frequency bands provided by the same cellular network, on the second connection; when transmitting or receiving the signals in the first frequency band on the first connection, breaking the second connection between the signal generating chip and the signal processing chip; and when transmitting or receiving the signals in the second frequency band on the second connection, breaking the first connection between the signal generating chip and the signal processing chip.

ADAR does not disclose the combination of features recited in proposed amended claim 14. For example, ADAR does not disclose breaking a second connection between a signal generating chip and a signal processing chip when transmitting or receiving signals in a first frequency band on a first connection; and breaking the first connection between the signal generating chip and the signal processing chip when transmitting or receiving signals in a second frequency band on the second connection, as recited in claim 14.

ADAR illustrates several embodiments of an amplifying apparatus in the figures. With regard to Figure 8, ADAR discloses that the amplifying apparatus includes a GaAs MMIC power amplifier chip 302. ADAR does not specifically disclose that the amplifying apparatus or power amplifier chip 302 is connected to a signal generating chip via first and second connections. In the embodiment of Figure 5A, for example, ADAR discloses a 800 MHz input 174 and a 1900 MHz input 176. Even assuming, for the sake of argument, that it is reasonable to interpret the disclosure of ADAR as disclosing a signal generating chip as the component generating the 800 MHz input 174 and the 1900 MHz input 176, that the 800 MHz input 174 is provided via a first connection, and that the 1900 MHz input 176 is provided via a second connection (points that Applicants do not concede), ADAR does not disclose breaking the second connection between the signal generating chip and the signal processing chip when transmitting or receiving the signals in the first frequency band on the first connection, and breaking the first connection between the signal generating chip and the signal processing chip when

transmitting or receiving the signals in the second frequency band on the second connection, as recited in claim 14.

Rather, ADAR discloses switches 190 and 192 that are provided as part of the amplifying apparatus (Figure 5A; col. 9, lines 57-62). Thus, the switches 190 and 192, of ADAR, cannot break a first connection provided between a signal generating chip and a signal processing chip, or a second connection provided between the signal generating chip and the signal processing chip, as recited in claim 14. Therefore, ADAR does not disclose breaking the second connection between the signal generating chip and the signal processing chip when transmitting or receiving the signals in the first frequency band on the first connection; and breaking the first connection between the signal generating chip and the signal processing chip when transmitting or receiving the signals in the second frequency band on the second connection, as recited in claim 14.

For at least the foregoing reasons, Applicants respectfully submit that claim 14 is not anticipated by ADAR. Claim 28 depends from claim 14 and is, therefore, not anticipated by ADAR for at least the reasons given with regard to claim 14.

Amended independent claims 16, 17, 22, 23, and 27 recite features similar to, yet possibly different in scope than, features described above with regard to claim 14. Accordingly, claims 16, 17, 22, 23, and 27 are not anticipated by ADAR for at least reasons similar to the reasons given with regard to claim 14.

Claims 19-21 and 29 depend from claim 17, and claims 24-26 depend from claim 23. Without acquiescing in the Examiner's rejection of claims 19-21, 24-26, and 29,

Applicants submit that claims 19-21, 24-26, and 29 are not anticipated by ADAR for at least the reasons given with regard to claims 17 and 23.

Accordingly, Applicants respectfully request the Examiner's reconsideration and withdrawal of the rejection of claims 14, 16, 17, and 19-29 under 35 U.S.C. § 102 based on ADAR.

CONCLUSION

In view of the foregoing amendments and remarks, Applicants respectfully request the Examiner's reconsideration of the application and the timely allowance of the pending claims.

Applicants respectfully request that this Amendment under 37 C.F.R. § 1.116 be entered by the Examiner, placing claims 14, 16, 17, and 19-29 in condition for allowance. Applicants submit that this Amendment should allow for immediate action by the Examiner. Further, Applicants submit that the entry of this Amendment would place the application in better form for appeal, should the Examiner dispute the patentability of the pending claims.

As Applicants' remarks with respect to the Examiner's rejections overcome the rejections, Applicants' silence as to certain assertions by the Examiner in the Office Action or certain requirements that may be applicable to such rejections (e.g., whether a reference constitutes prior art, reasons for modifying a reference and/or combining references, assertions as to dependent claims, etc.) is not a concession by Applicants that

such assertions are accurate or such requirements have been met, and Applicants reserve the right to dispute these assertions/requirements in the future.

While the present application is now believed to be in condition for allowance, should the Examiner find some issue that remains unresolved, or should any new issues arise which could be eliminated through a discussion with Applicants' representative, then the Examiner is invited to contact the undersigned by telephone in order to expedite further prosecution of this application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. § 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 50-1070 and please credit any excess fees to such deposit account.

Respectfully submitted,

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